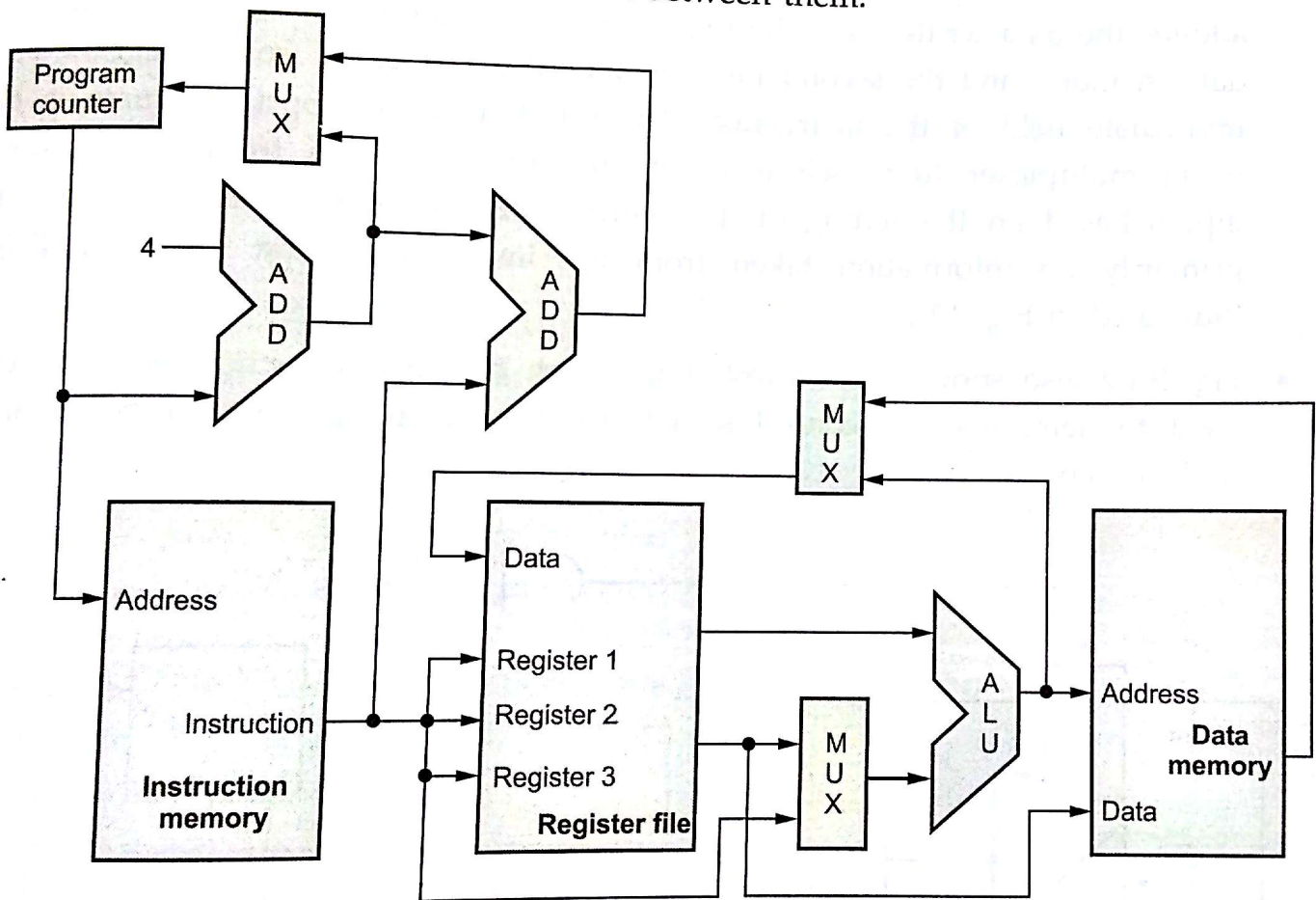


Fig. 3.1.1 shows the block diagram of a MIPS implementation, showing the functional units and interconnection between them.



**Fig. 3.1.1 Major functional units and interconnections between them for implementation of MIPS subset**

**Operation**