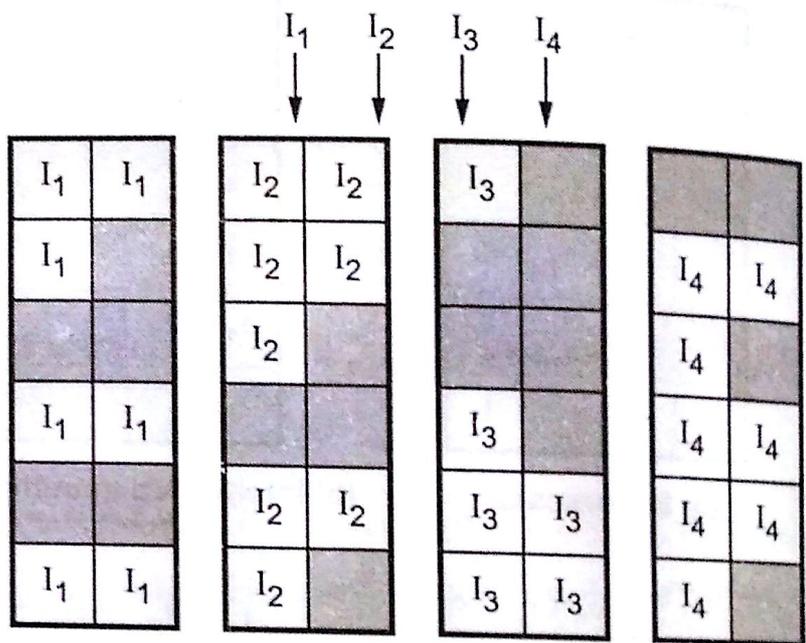


(a) Simultaneous multithreading (SMT)



(b) Chip multiprocessor (Multicore)

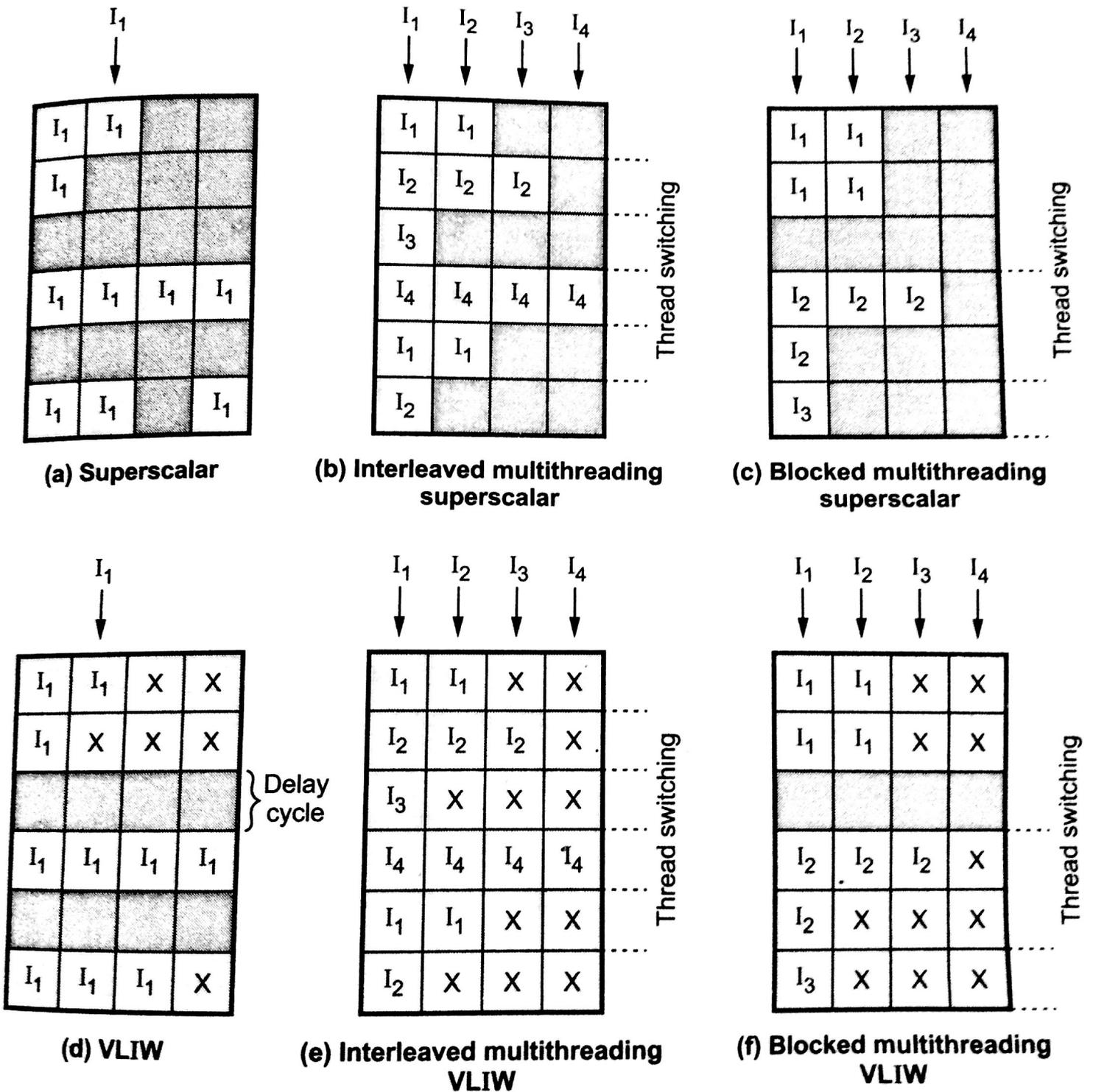


Fig. 4.4.2 Approaches to execute multiple threads

pipeline is used like in RISC and CISC machines.

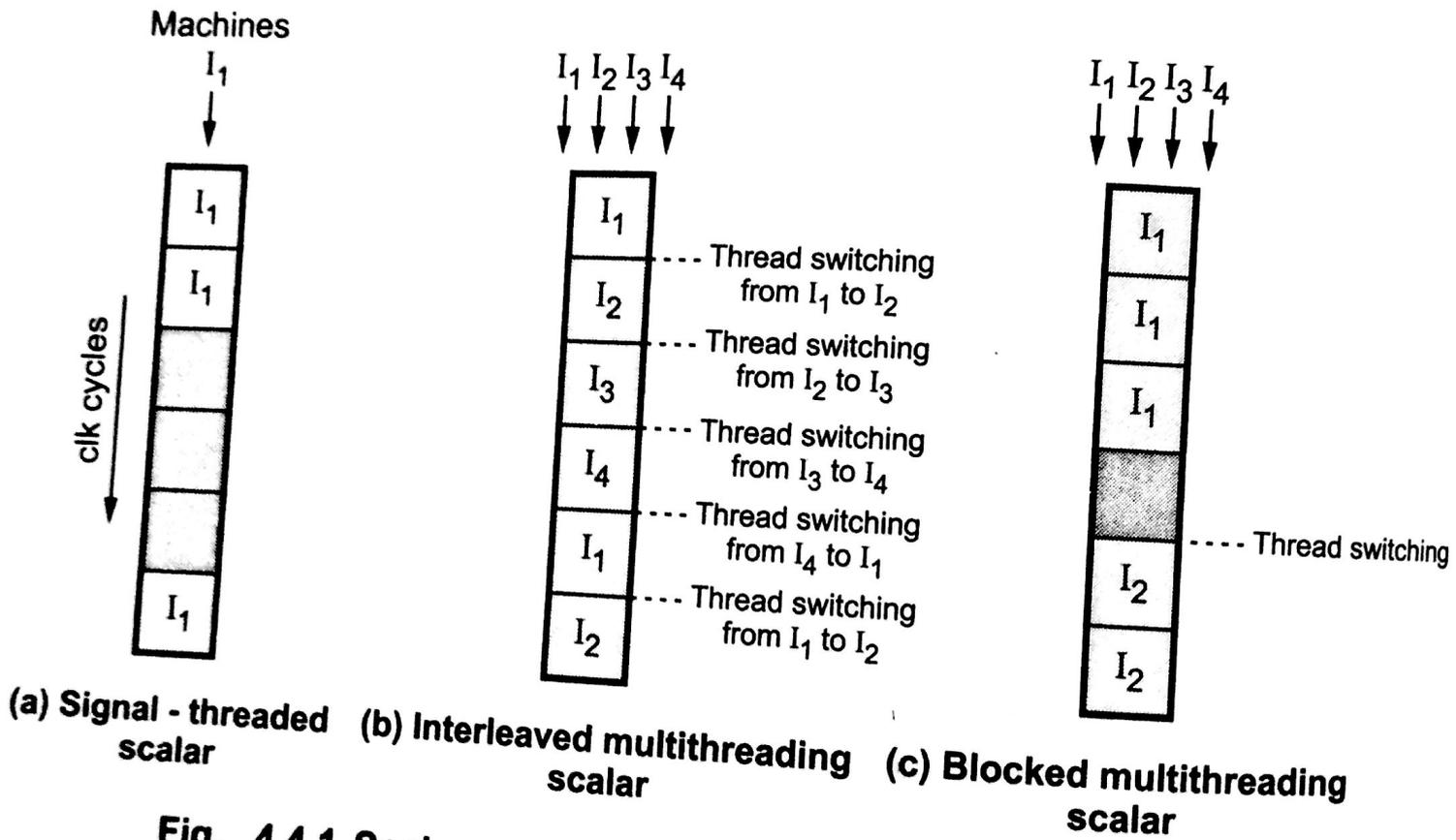


Fig. 4.4.1 Scalar approaches for threads scalar execution